



Attorney Docket No. 50047050-0011

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

re Patent Application of

Richard A. Mann, et al.

Application No. 09/680,036

Filed: **October 5, 2000**

For: **TAPERED THRESHOLD RESET FET FOR
CMOS IMAGERS**

) Group Art Unit 2811

) Examiner: **Kang, Donghee**

I hereby certify that this document is being deposited with the United States Postal Service as first class mail in an envelope addressed to: BOX FEE AMENDMENT, Commissioner for Patents, Washington, D.C. 20231 on December 16, 2002.

Antonietta Musto
Antonietta Musto

BOX FEE AMENDMENT
Commissioner for Patents
Washington, D.C. 20231

RESPONSE TO JULY 15, 2002 OFFICE ACTION

Dear Sir:

This Amendment is submitted in response to the Office Action mailed July 15, 2002. Applicant respectfully requests amendment of the patent application, and reconsideration and allowance of the pending claims.

Please revise the Attorney Docket No. from "09785980-0021" to --50047050-0011--.

IN THE SPECIFICATION

On page 7; the paragraph starting at line 4 is amended as follows:

The transistor 104 of sensor 100 further includes a material in the space defined by the separation of the source 106 from the drain 110 beneath the gate 108. In an example implementation of the transistor 104, the length 206 of the material is at least 20 percent longer than a process minimum. For example, the process minimum gate length for a conventional 3.3 volt logic process range is approximately .35 microns. The recommended minimum gate length to avoid exaggerated short channel effects would be approximately 0.4 microns. While

#7/AndHB
R. 4
11/7/03

RECEIVED
DEC 27 2002
TECHNOLOGY CENTER 2800